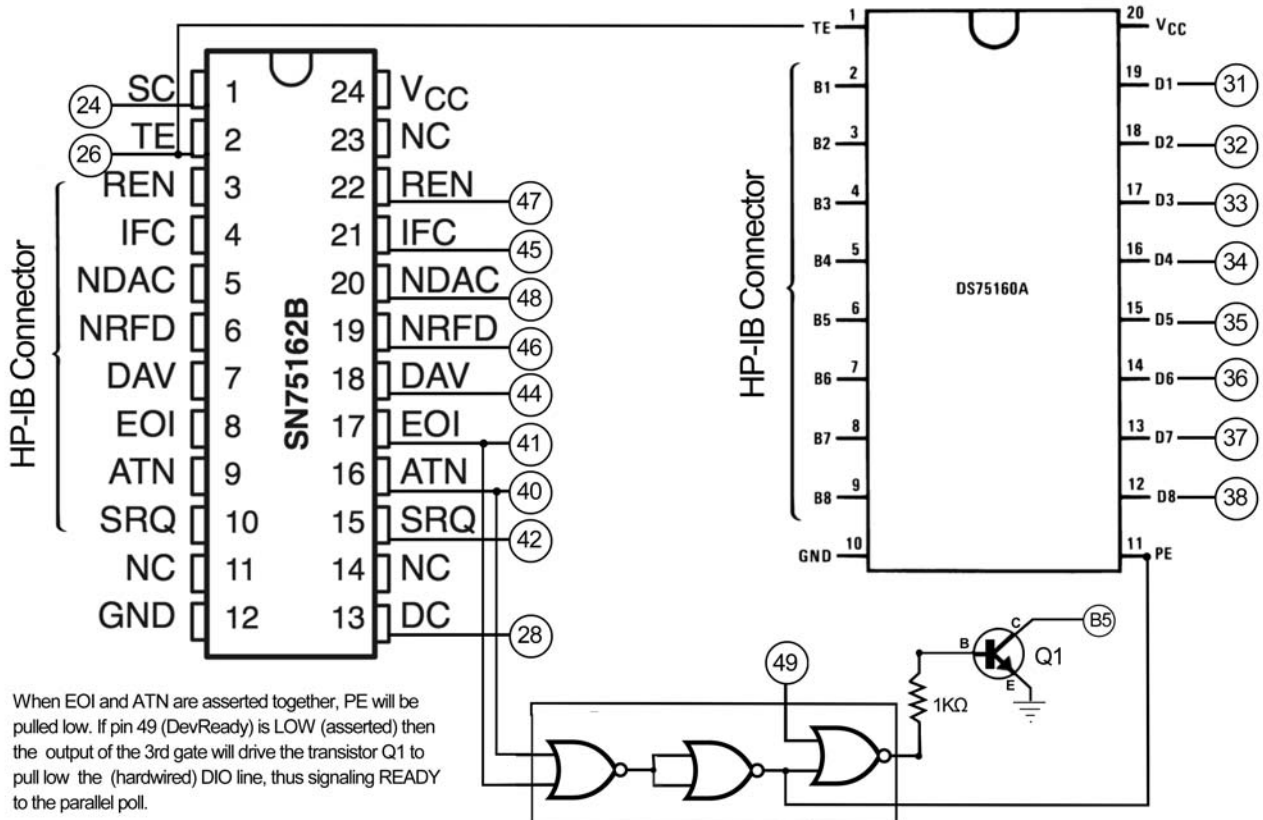


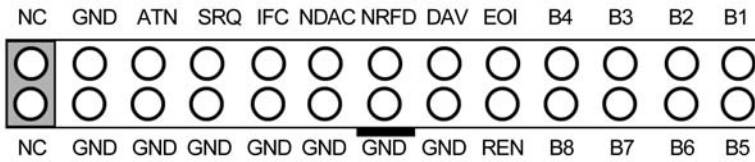
Arduino-Mega HP-IB Interface



When EOI and ATN are asserted together, PE will be pulled low. If pin 49 (DevReady) is LOW (asserted) then the output of the 3rd gate will drive the transistor Q1 to pull low the (hardwired) DIO line, thus signaling READY to the parallel poll.

NOTE 1: DIO 5 corresponds to HP-IB address 3.

NOTE 2: Pins 50..53 are used by the Ethernet Shield (SPI)



HP-IB Connector

